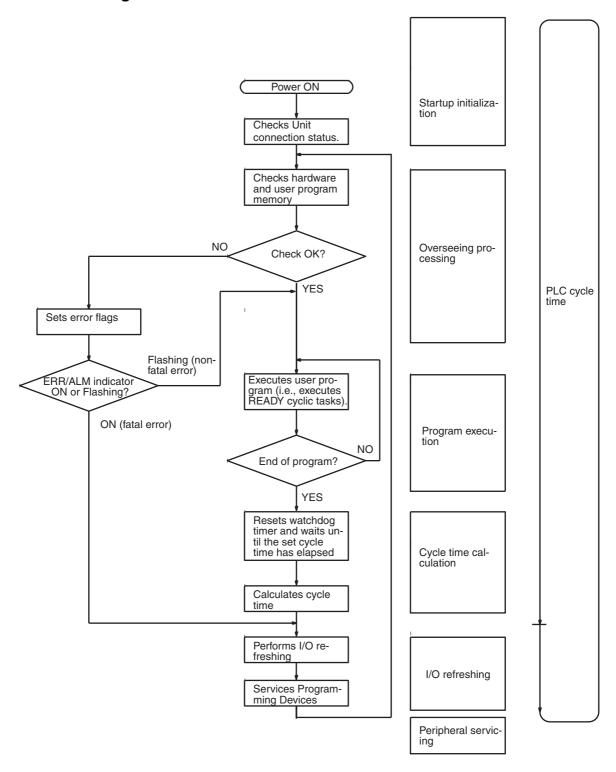
# 10-4 Computing the Cycle Time

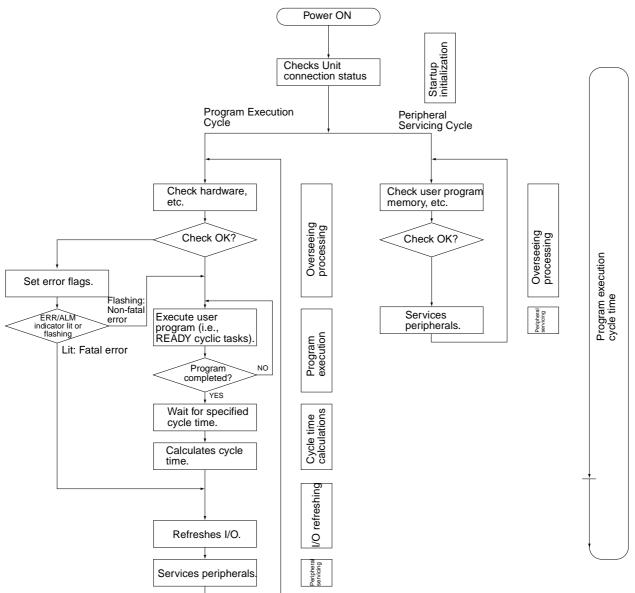
# 10-4-1 CPU Unit Operation Flowchart

The CS-series CPU Units process data in repeating cycles from the overseeing processing up to peripheral servicing as shown in the following diagram.

### **Normal Processing Mode**



#### **Parallel Processing Mode**



## 10-4-2 Cycle Time Overview

### **Normal Processing Mode**

The cycle time depends on the following conditions.

- Type and number of instructions in the user program (in all cyclic tasks that are executed during a cycle, and within interrupt tasks for which the execution conditions have been satisfied).
- Type and number of Basic I/O Units
- Number of SYSMAC BUS Remote I/O Master Units and number of I/O points on the Slaves
- Type and number of Special I/O Units, CS-series CPU Bus Units, Inner Boards, and type of services being executed.
- Specific servicing for the following Units/Boards
  - Data link refreshing and the number of data link words for Controller Link and SYSMAC LINK Units